

100mA / 3-TERMINAL NEGATIVE VOLTAGE REGULATOR

DESCRIPTION

The AMC79L05 is a 3-terminal fixed negative-voltage designed for a wide range of applications. This regulator can provide local on card regulation, eliminating the distribution problems associated with single point regulation. In addition, it can be used with power-pass elements to make high-current voltage regulators with 100mA output current.

Protection features such as thermal shutdown and current limiting have been designed internally which will protect the device from damage in case of overload or overheating.

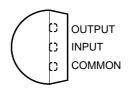
FEATURES

- ±5% tolerance of output voltage
- **■** Wide input range
- Internal thermal overload protection
- Output current > 100mA
- No External Components
- Short circuit protection
- Available in 3L plastic TO-92 and plastic 8 pin S.O.I.C.
- Identical pin assignment to earlier 79L05 series.

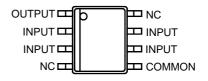
APPLICATIONS

- Logic Systems
- Computer Add-On Cards
- Modem Cards
- Power Suppliers

PACKAGE PIN OUT



3-Pin Plastic TO-92 (Top View)



8-Pin S.O.I.C Surface Mount (Top View)

ORDER INFORMATION					
T _A (°C)	LP Plastic TO-92 3-pin	Plastic SO-8 8-pin			
0 to 70	AMC79L05LP	AMC79L05DM			
0 to 70	AMC79L05LP(Lead Free)	AMC79L05DM(Lead Free)			

Note: 1.All surface-mount and TO-92 packages are available in Tape & Reel. Append the letter "T" to part number (i.e. AMC79L05LPT or AMC79L05DMT).

2.For TO-92 in Tape & Box (without reel), add suffix "TB" (i.e. AMC79L05LPTB).

3. The letter "F" is marked for Lead Free process.





ABSOLUTE MAXIMUM RATINGS (Note 1)	
Input Voltage	-30V
Operating free-air temperature range, T _A	0°C to 70°C
Storage temperature range	-65°C to 150°C
Lead temperature (soldiering, 10 seconds)	260°C
Note 1: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. out of the specified terminal.	Currents are positive into, negative

THERMAL DATA					
LP PACKAGE:					
Thermal Resistance-Junction to Ambient, θ_{JA}	156 °C/W				
DM PACKAGE:					
Thermal Resistance-Junction to Ambient, θ_{JA}	165 °C/W				
Junction Temperature Calculation: $T_J = T_A + (P_D \times \theta_{JA})$.					
The θ_{JA} numbers are guidelines for the thermal performance of the device/pc-board system.					
All of the above assume no ambient airflow.					



RECOMMENDED OPERATING CONDITIONS								
Downwater	Symbol	Recommended Operating Conditions			TT. 14.			
Parameter		Min.	Тур.	Max.	Units			
Input Voltage	$V_{\rm I}$	-7		-20	V			
Output Current	I_{OUT}			100	mA			
Operating Virtual Junction Temperature	T_{J}	0		125	°C			

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, these specifications in **bold type** apply over the operating temperature range of $0^{\circ}\text{C} \leq T_{J} \leq +150^{\circ}\text{C}$, $V_{IN} = -10\text{V}$, $I_{OUT} = 40\text{mA}$, $C_{IN} = 0.33\mu\text{F}$, $C_{OUT} = 0.1\mu\text{F}$, and are for DC characteristics only. (Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

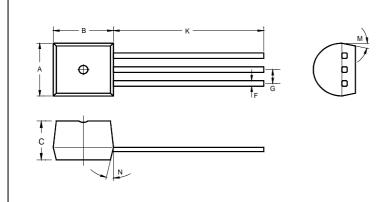
Parameter	Crymbal	Test Conditions	AMC79L05			Units	
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Omts	
		$T_{J} = 25 ^{\circ}\text{C}$ $-7\text{V} \le \text{V}_{\text{IN}} \le -20\text{V}, \ 1\text{mA} \le \text{I}_{\text{OUT}} \le 40\text{mA}$		-5.0	-5.2		
Output Voltage	V_{OUT}				-5.25	V	
		$1 \text{mA} \le I_{\text{OUT}} \le 70 \text{mA}$	-4.75 -5.25				
Line Regulation	$\triangle_{\mathbf{V}_{\mathrm{OI}}}$	$-7V \le V_{IN} \le -20V, T_J = 25$ °C		15	150	mV	
Line Regulation	— v _{OI}	$-8V \le V_{IN} \le -20V, T_J = 25$ °C			100	III V	
Load Dogulation	$\triangle V_{OL}$	$1mA \leq I_{OUT} \leq 100mA, T_J = 25^{\circ}C$		20	60	mV	
Load Regulation	△V _{OL}	$1 \text{mA} \le I_{\text{OUT}} \le 40 \text{mA}, T_{\text{J}} = 25 ^{\circ}\text{C}$		10	30	mV	
Peak Output Current	eak Output Current I_{PEAK} $T_J = 25$ °C			140		mA	
Dropout Voltage		$T_J = 25$ °C		1.7		V	
Quiescent Current	т	$T_J = 25$ °C	25°C		6.6	m A	
Quiescent Current	I_Q	$T_J = 125$ °C			6.0	– mA	
Ouissaant Current Change	A T	$-8V \le V_{IN} \le -20V$			1.5	mA	
Quiescent Current Change	$\triangle I_Q$	$1 \text{mA} \le I_{\text{OUT}} \le 40 \text{mA}$			0.1	ША	
Ripple Rejection (note 2)	R_R	$f = 120$ Hz, $-8V \le V_{IN} \le -18V$, $T_J = 25$ °C	41	49		dB	
Output Noise Voltage (note 2)	V _{ORMS}	10 Hz \leq f \leq 100KHz, $T_J = 25$ $^{\circ}$ C		40		μV	

Note 2: These parameters, although guaranteed, are not 100% tested in production prior to shipment



PACKAGE

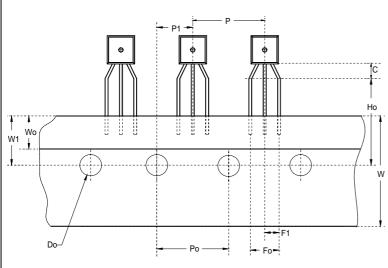
3-Pin Plastic TO-92



Note: For TO-92 in tape & reel, refer to TO-92 package and carrier dimension data for lead dimensions.

		NCHES	3	MILLIMETERS		
	MIN	TYP	MAX	MIN	TYP	MAX
Α	0.175	0.180	0.205	4.45	4.57	5.21
В	0.170	0.180	0.210	4.32	4.57	5.33
С	0.125	0.142	0.165	3.18	3.62	4.19
F	ı	0.015	ı	ı	0.38	1
G	ı	0.050	ı	ı	1.27	1
J	ı	0.150	ı	ı	3.81	1
K	0.500	0.580	-	12.70	14.73	-
М	-	5°	-	-	5°	-
N	-	5°	-	-	5º	-

3-Pin Plastic TO-92 Package (Taped and Reeled) and Carrier Dimensions

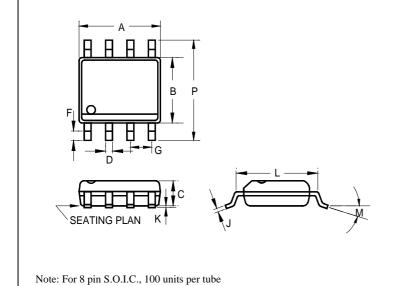


Note: For 3L	TO92,	2,000	units	per	Reel

	I	NCHES	3	MILLIMETERS		
	MIN	TYP	MAX	MIN	TYP	MAX
С	0.079	-	-	2.00	-	-
Р	0.480	0.500	0.520	12.2	12.7	13.2
Ро	0.488	0.500	0.512	12.4	12.7	13.0
Do	0.150	0.157	0.165	3.8	4.0	4.2
P1	0.230	0.250	0.256	5.85	6.35	6.85
Fo	0.165	0.197	0.220	4.2	5.0	5.6
W	0.669	0.709	0.748	17.0	18.0	19.0
Но	0.610	0.630	0.649	15.5	16.0	16.5
Wo	0.224	0.236	0.248	5.7	6.0	6.3
W1	0.335	0.354	0.374	8.5	9.0	9.5



8-Pin Plastic S.O.I.C.



	I	NCHES	3	MIL	LIMETE	ERS
	MIN	TYP	MAX	MIN	TYP	MAX
Α	0.183	-	0.202	4.65	-	5.13
В	0.144	ı	0.163	3.66	1	4.14
С	0.068	ı	0.074	1.73	1	1.88
D	0.010	ı	0.020	0.25	1	0.51
F	0.015	ı	0.035	0.38	1	0.89
G	0.	050 BS	C	1.27 BSC		
J	0.007	ı	0.010	0.19	ı	0.25
K	0.005	ı	0.010	0.13	ı	0.25
L	0.189	ı	0.205	4.80	ı	5.21
М	-		8º		-	8º
Р	0.228	-	0.244	5.79	-	6.20



IMPORTANT NOTICE

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